LIGHTWAVE LINK INC.



State Same

4x8 Optical Switch

Product Description

Lightwave Link Inc. 4x8 optical switch is designed for use in optical fiber communication networks, measurement instruments and PCI-E Cards. The switch consists of two ports that selectively transmits, redirects, or blocks optical power in a fiber optic transmission line. The optical switch must be actuated to select or change between two states. Furthermore, for the Latching type, it only takes an electrical pulse width with duration \geq 20msec to change the state. As a result, it consumes low electric energy to operate the optical switch. Lightwave Link Inc. 4x8 optical switch fully complies with RoHS Directive 2002/95/EC (2008/385/EC).



- Smallest Size
- Low Insertion-Loss
- Fast Switching Speed
- PCB Mountable
- Available in Single Mode / Multi Mode
- RoHS Compliance

Performance Specification

Applications

- Optical network protection and restoration
- Optical network monitoring
- Reconfigurable add/drop multiplexers
- Transmission equipment protection
- Research and development
- Wavelength router

	9µm	Core Single M	Node	50µm or 62	2.5µm Core	Multi Mode	د! سارا
Parameter	Min.	Тур.	Max.	Min.	Max.	Unit	
Wavelength Range ¹	1260~1630			Min. Typ. Max. 850/1300			nm
Insertion Loss ²			1.0			0.6	dB
Return Loss		-50					dB
PDL			0.1				dB
WDL			0.3				dB
Crosstalk		-80			-80		dB
Repeatability			±0.1			±0.1	dB
Switching Time ³			3.5			3.5	ms
Absolute Optical Input Power			500			500	mW
Operating Voltage	4.5	5.0	5.5	4.5	5.0	5.5	VDC
Power Consumption	Latching: 200±10% / Non-Latching: 140±10%						
Switching Life Expectancy	3x10 ⁷			3x10 ⁷			Cycles
Operation Temperature-Norma	-5		70	-5		70	°C
Operation Temperature-Special	-20		70	-20		70	°C
Storage Temperature	-40		85	-40		85	°C
Operation Humidity	5		85	5		85	%RH
Storage Humidity	5		85	5		85	%RH
Dimension (H*W*L)	7.6 x 11 x 22.6						
Weight ⁴	10						

1.Special wavelength would be upon request.

2.Optical parameters excluded connectors.

3.A minimum \geq 20ms pulse is recommended for latching type of switch.

4. The product weight excluded optical connectors.



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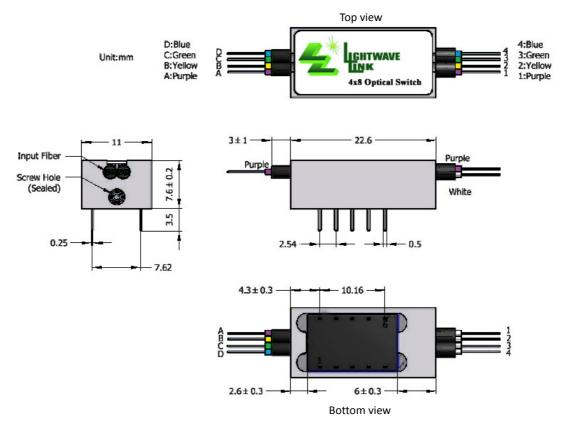
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Function Diagram



Physical Dimension



PIN Description

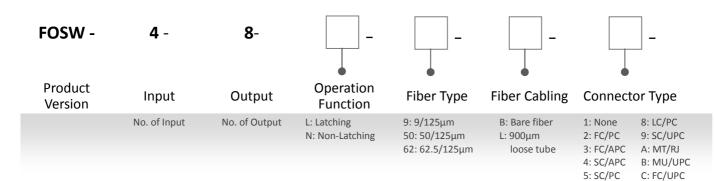
Pin Number	Latching Pin Function	Non-Latching Pin Function		
1	Quad Ch 1 activation terminal(+)	N/C		
2	Quad Ch 2 Monitor	Quad Ch 2 Monitor		
3	Monitor Common	Monitor Common		
4	Quad Ch 1 Monitor	Quad Ch 1 Monitor		
5	Quad Ch 1 activation terminal(-)	Quad Ch 2 activation terminal(+)		
6	Quad Ch 2 activation terminal(-)	Quad Ch 2 activation terminal(-)		
7	Quad Ch 1 Monitor	Quad Ch 1 Monitor		
8	Monitor Common	Monitor Common		
9	Quad Ch 2 Monitor	Quad Ch 2 Monitor		
10	Quad Ch 2 activation terminal(+)	N/C		



Operation of the Optical Switch

Relay Type	PIN OSW State	1	5	6	10	PIN Connection	Remark
Latching Type	A Ch1	н	L	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	
	B Ch1	Н	L	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	
	C Ch1	Н	L	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	
	D Ch1	Н	L	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	
	A Ch2	-	-	L	Н	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	B Ch2	-	-	L	Н	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	C Ch2	-	-	L	Н	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	D Ch2	-	-	L	Н	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
Non-Latching Type	A Ch1	-	-	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	Default
	B Ch1	-	-	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	Default
	C Ch1	-	-	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	Default
	D Ch1	-	-	-	-	3 , 4 pin closed ; 2 , 3 pin open 7 , 8 pin closed ; 8 , 9 pin open	Default
	A Ch2	-	Н	L	-	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	B Ch2	-	н	L	-	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	C Ch2	-	н	L	-	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	
	D Ch2	-	н	L	-	2 , 3 pin closed ; 3 , 4 pin open 8 , 9 pin closed ; 7 , 8 pin open	

Ordering Information



E: LC/UPC

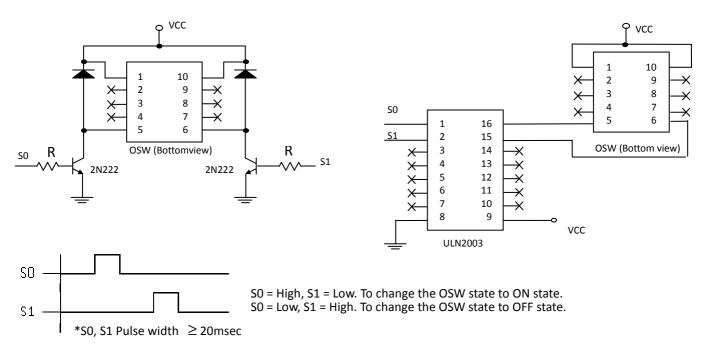
6: MU/PC D: LC/APC

7: ST/PC

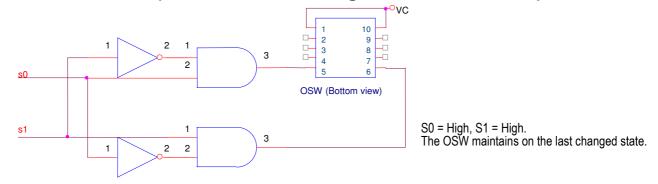


Application Circuitry for Latching Type

To provide sufficient power to switch, two application circuits using 2N2222 BJT and ULN2003 Darlington pair IC are showed below.



The Recommend Circuitry for So and S1 Stand High Level Simultaneously



Application Circuitry for Non-Latching Type

To provide sufficient power to switch, two application circuits using 2N2222 BJT and ULN2003 Darlington pair IC are showed below.

