## 4x8 Optical Switch

## Product Description

Lightwave Link Inc． $4 \times 8$ optical switch is designed for use in optical fiber communication networks，measurement instruments and PCI－E Cards．The switch consists of two ports that selectively transmits，redirects，or blocks optical power in a fiber optic transmission line．The optical switch must be actuated to select or change between two states．Furthermore，for the Latching type，it only takes an electrical pulse width with duration $\geqq 20 \mathrm{msec}$ to change the state．As a result，it consumes low electric energy to operate the optical switch．Lightwave Link Inc．4x8 optical switch fully complies with RoHS Directive 2002／95／EC（2008／385／EC）．


Features
－Smallest Size
－Low Insertion－Loss
－Fast Switching Speed
－PCB Mountable
－Available in Single Mode／Multi Mode
－RoHS Compliance

## Applications

－Optical network protection and restoration
－Optical network monitoring
－Reconfigurable add／drop multiplexers
－Transmission equipment protection
－Research and development
－Wavelength router

## Performance Specification



1．Special wavelength would be upon request．
2．Optical parameters excluded connectors．
3．A minimum $\geqq 20 \mathrm{~ms}$ pulse is recommended for latching type of switch．
4．The product weight excluded optical connectors．

## Function Diagram



## Physical Dimension



## PIN Description

| Pin Number | Latching Pin Function | Non-Latching Pin Function |
| :--- | :--- | :--- |
| 1 | Quad Ch 1 activation terminal( + ) | N/C |
| 2 | Quad Ch 2 Monitor | Quad Ch 2 Monitor |
| 3 | Monitor Common | Monitor Common |
| 4 | Quad Ch 1 Monitor | Quad Ch 1 Monitor |
| 5 | Quad Ch 1 activation terminal( -$)$ | Quad Ch 2 activation terminal(+) |
| 6 | Quad Ch 2 activation terminal(-) | Quad Ch 2 activation terminal(-) |
| 7 | Quad Ch 1 Monitor | Quad Ch 1 Monitor |
| 8 | Monitor Common | Monitor Common |
| 9 | Quad Ch 2 Monitor | Quad Ch 2 Monitor |
| 10 | Quad Ch 2 activation terminal(+) | N/C |

## Operation of the Optical Switch

| Relay <br> Type | PIN <br> OSW State | 1 | 5 | 6 | 10 | PIN Connection | Remark |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Latching Type | A Ch1 | H | L | - | - | 3,4 pin closed ; 2,3 pin open <br> 7,8 pin closed; 8,9 pin open |  |
|  | B Ch1 | H | L | - | - | 3,4 pin closed ; 2,3 pin open 7,8 pin closed ; 8,9 pin open |  |
|  | C Ch1 | H | L | - | - | 3,4 pin closed ; 2,3 pin open 7,8 pin closed ; 8,9 pin open |  |
|  | D Ch1 | H | L | - | - | 3,4 pin closed ; 2,3 pin open 7,8 pin closed ; 8,9 pin open |  |
|  | A Ch2 | - | - | L | H | 2, 3 pin closed ; 3,4 pin open 8,9 pin closed ; 7,8 pin open |  |
|  | B Ch2 | - | - | L | H | 2,3 pin closed ; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |
|  | C Ch2 | - | - | L | H | 2,3 pin closed; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |
|  | D Ch2 | - | - | L | H | 2,3 pin closed; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |
| Non-Latching Type | A Ch1 | - | - | - | - | 3,4 pin closed ; 2,3 pin open <br> 7,8 pin closed; 8,9 pin open | Default |
|  | B Ch1 | - | - | - | - | 3,4 pin closed ; 2,3 pin open <br> 7,8 pin closed ; 8,9 pin open | Default |
|  | C Ch1 | - | - | - | - | 3,4 pin closed ; 2,3 pin open 7,8 pin closed ; 8,9 pin open | Default |
|  | D Ch1 | - | - | - | - | 3,4 pin closed ; 2,3 pin open <br> 7,8 pin closed ; 8,9 pin open | Default |
|  | A Ch2 | - | H | L | - | 2,3 pin closed; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |
|  | B Ch2 | - | H | L | - | 2,3 pin closed ; 3,4 pin open <br> 8,9 pin closed; 7,8 pin open |  |
|  | C Ch2 | - | H | L | - | 2,3 pin closed ; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |
|  | D Ch2 | - | H | L | - | 2,3 pin closed ; 3,4 pin open 8,9 pin closed; 7,8 pin open |  |

## Ordering Information

| SW - | 4 - | 8- | - | - | - |  | - |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Product Version | Input | Output | Operation Function | Fiber Type | Fiber Cabling | Connec | Type |
|  | No. of Input | No. of Output | L: Latching | 9:9/125 $\quad$ m | B: Bare fiber | 1: None | 8: LC/PC |
|  |  |  | N: Non-Latching | 50: $50 / 125 \mu \mathrm{~m}$ | L: $900 \mu \mathrm{~m}$ | 2: FC/PC | 9: SC/UPC |
|  |  |  |  | 62: $62.5 / 125 \mu \mathrm{~m}$ | loose tube | 3: FC/APC | A: MT/RJ |
|  |  |  |  |  |  | 4: SC/APC | B: MU/UPC |
|  |  |  |  |  |  | 5: SC/PC | C: FC/UPC |
|  |  |  |  |  |  | 6: MU/PC | D: LC/APC |
|  |  |  |  |  |  | 7: ST/PC | E: LC/UPC |

## Application Circuitry for Latching Type

To provide sufficient power to switch, two application circuits using 2N2222 BJT and ULN2003 Darlington pair IC are showed below.


SO = High, S1 = Low. To change the OSW state to ON state. S0 = Low, S1 = High. To change the OSW state to OFF state.

The Recommend Circuitry for So and S1 Stand High Level Simultaneously


S0 $=$ High, S1 $=$ High.
The OSW maintains on the last changed state.

## Application Circuitry for Non-Latching Type

To provide sufficient power to switch, two application circuits using 2N2222 BJT and ULN2003 Darlington pair IC are showed below.


$\mathrm{SO}=$ Low. To change the OSW state to default mode(CH1).
$\mathrm{SO}=$ High. To change the OSW state to CH 2 .

